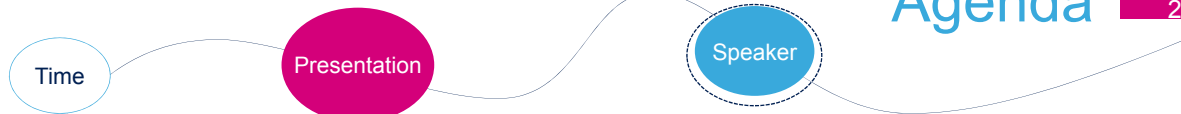


Agenda 2



9:00 am	Introduction	T. Sorensen
9:10	Welcome / Strategy & Vision	C. Bozotti
9:35	Business & Financial Roadmap	G. Penalver
10:05	Technology R&D & Manufacturing	J.M. Chery
10:35	PANEL: MEMS & Sensors	E. Aussedat, M. Cassis, F. Guibert, B. Vigna
11:00	Break	
11:20	PANEL: Automotive & Smart Power	P. Grimme , M. Monti, C. Papa
11:45	PANEL: MCU & Application Processors	G.L. Bertino, C. Dardanne, B. Krysiak,
12:10	Q & A Panel	C. Bozotti, J.M. Chery, C. Ferro, C. Papa, G. Penalver
1:00	Lunch	
2:30	Breakout Sessions	
5:00 to 6:30	Reception	



Agenda – Breakout Sessions 3

Breakout Rooms	AMS - IPD	DCG	IBP	APG	Plenary Room MMS	Manufacturing & TR&D - IPD
2:30 – 3:00	AMS	DCG	IBP	APG		Manufacturing & Technology R&D
3:00 – 3:30	IPD			APG	MMS	Manufacturing & Technology R&D
3:30 – 4:00	AMS	DCG	IBP			IPD
4:00 – 4:30		DCG		APG	MMS	IPD
4:30 – 5:00	AMS		IBP		MMS	Manufacturing & Technology R&D
5:00 – 6:30	Reception – Atrium / Mezzanine Floor					

- Analog, MEMS & Sensors (AMS)
- Automotive (APG)
- Digital Convergence (DCG)
- Imaging, BiCMOS ASIC & Silicon Photonics (IBP)
- Industrial & Power Discrete (IPD)
- Microcontrollers (MMS)
- Manufacturing & Technology R&D

Benedetto Vigna, Marco Cassis
 Marco Monti, Paul Grimme
 Gian Luca Bertino, Laurent Remont
 Eric Aussedat, Flavio Benetti
 Carmelo Papa, Matteo Lo Presti
 Claude Dardanne, Francois Guibert
 Jean-Marc Chery, Orio Bellezza

